

Dual RS-232 Level Shifter

MK-38824

December 12, 2014

IONOTRONICS Corporation

<http://www.ionotronics.com>

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1 Introduction

The MK-38824 provides RS-232 level shifting for CMOS GPIO signals. The device supports loop back tests with switches for easy testing and trouble shooting.

1.1 Specifications

- Dual RS-232 Level Translation
- Tx and Rx
- 4-pin Hirose Connectors
- Switch for Loop Back Test
- 5 or 3.3V Operation

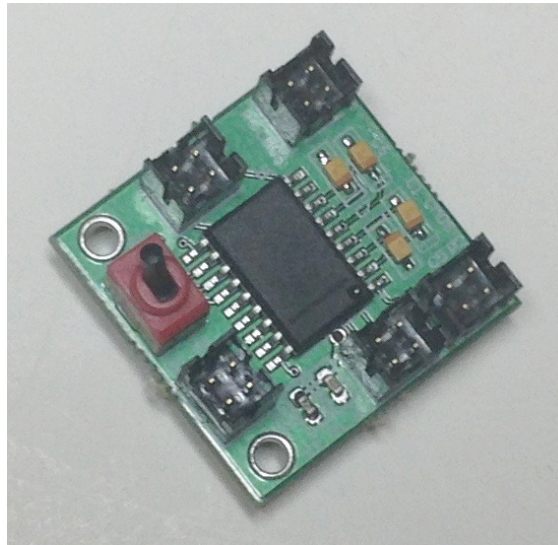


Figure 1 Top View MK-38824 Switch is for Loop Back Test

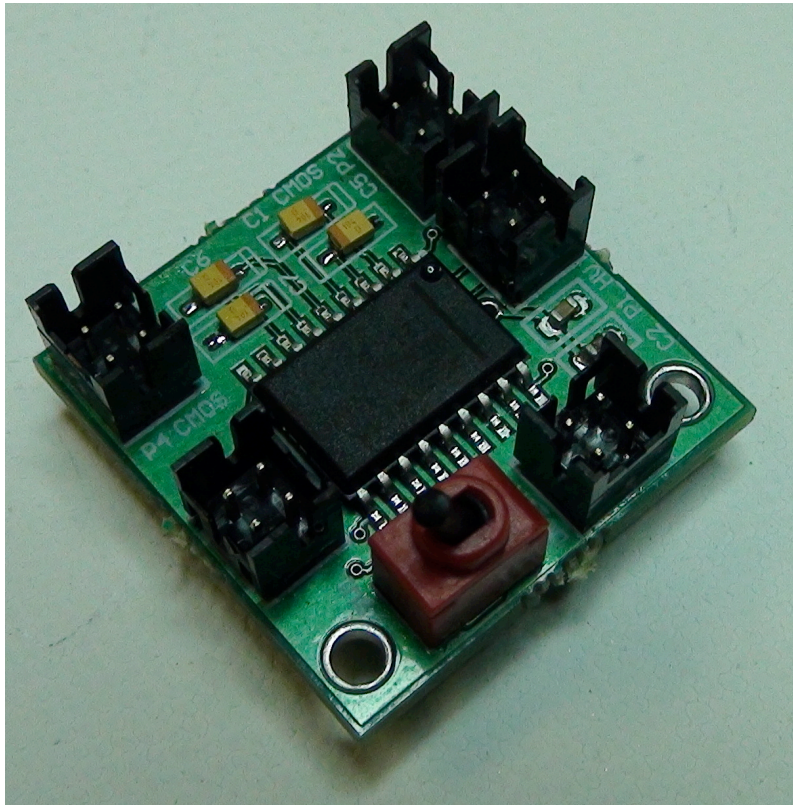


Figure 2 MK-38824

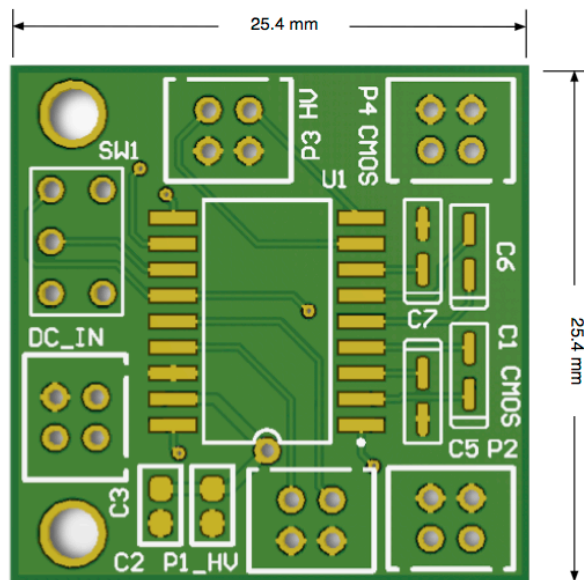


Figure 3 Board Dimensions

2 Connections

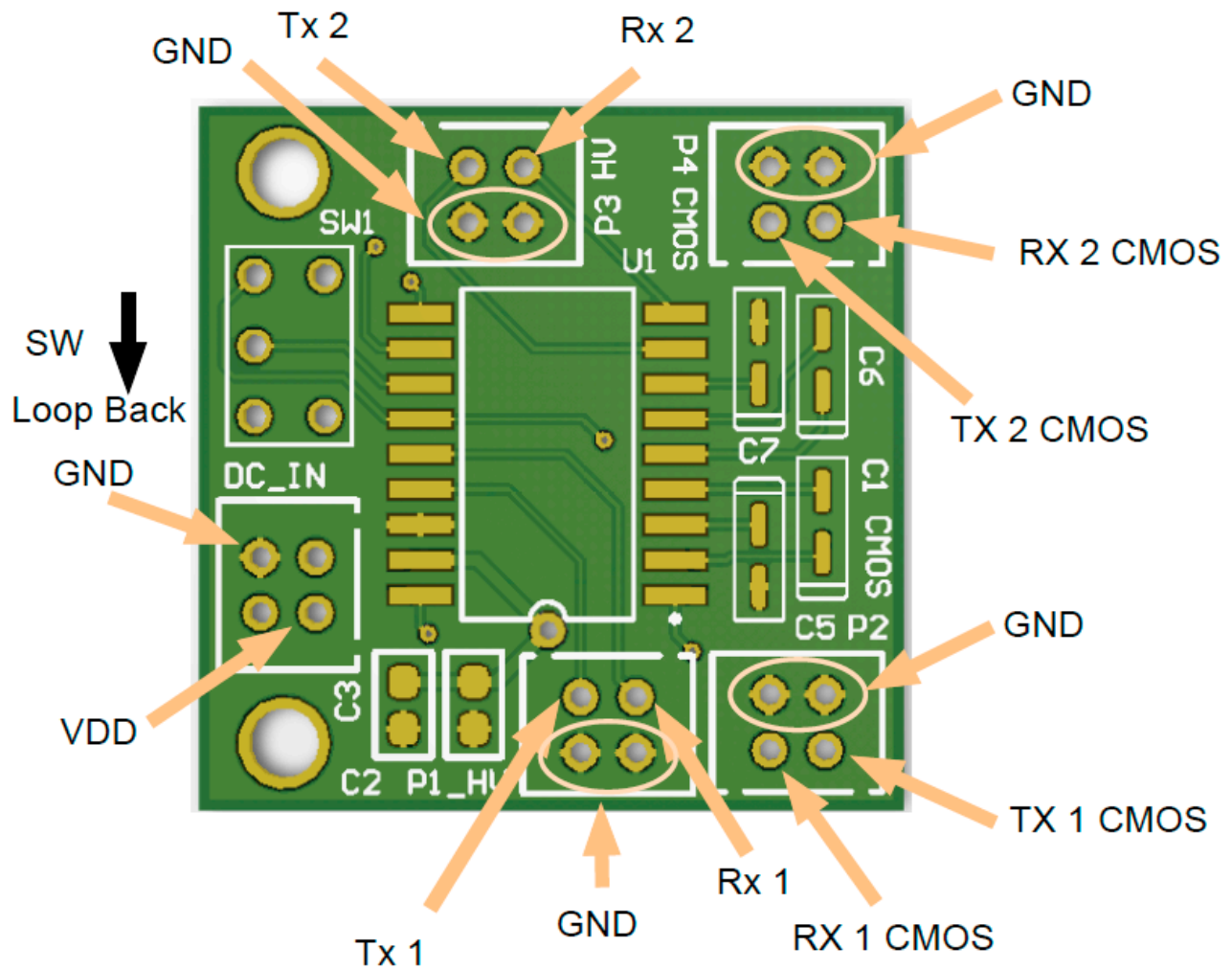


Figure 4 MK-38824 Connections

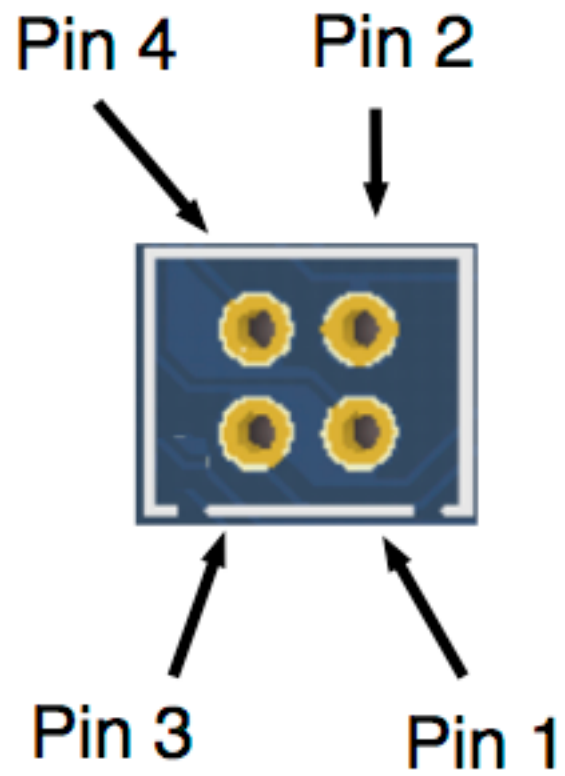


Figure 5 4 Pin DF11 Series Hirose Guide

3 Schematic

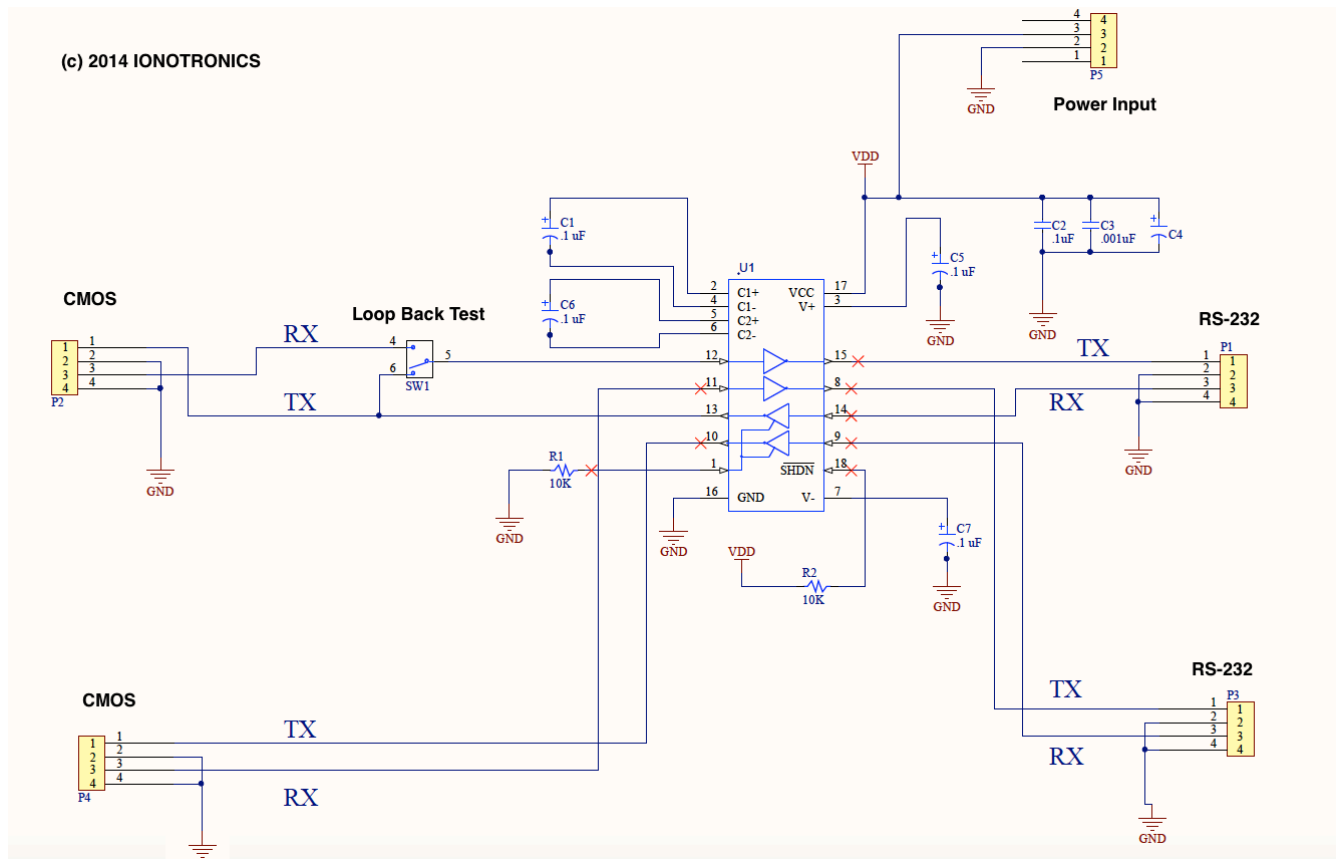


Figure 6 MK-38824 Dual RS-232 Level Shifter Schematic

4 Appendix Hirose DF11 Series Connectors

Figure 11 shows a 12 pin Hirose DF11 Series connector. This is useful when building cabling systems. Note pin 1 marker and numbering scheme.

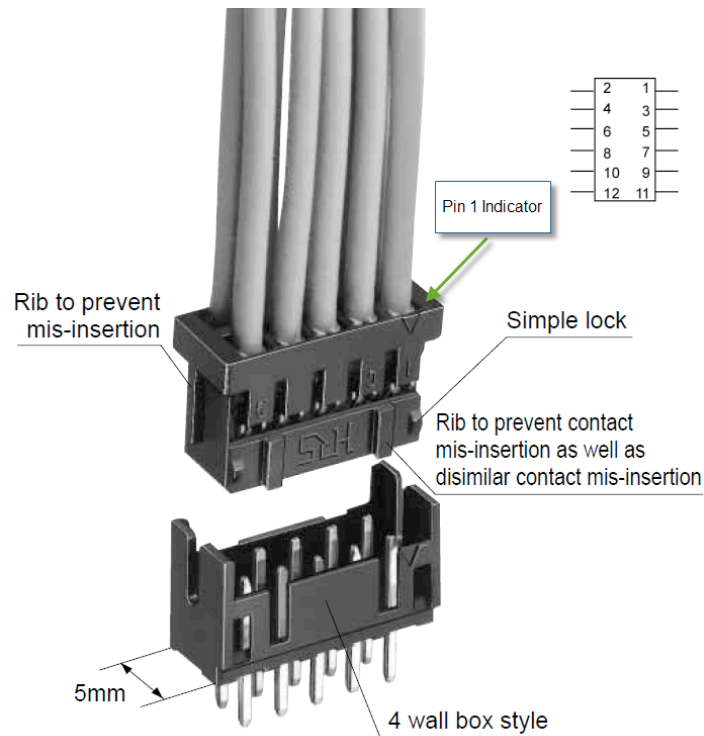


Figure 7 Guide to the Hirose DF11 series connector

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